

Table 4.2: BS2, BS2e, BS2sx and BS2p RAM Organization. Note: There are 16 words, consisting of two bytes each for a total of 32 bytes. All bits are individually addressable through modifiers and the bits within the upper three words are also individually addressable though the pre-defined names shown.

Word Name	Byte Names	Nibble Names	Bit Names	Special Notes
INS	INL INH	INA, INB INC, IND	IN0 – IN7 IN8 – IN15	Input pins
OUTS	OUTL OUTH	OUTA, OUTB OUTC, OUTD	OUT0 – OUT7 OUT8 – OUT15	Output pins
DIRS	DIRL DIRH	DIRA, DIRB DIRC, DIRD	DIR0 – DIR7 DIR8 – DIR15	I/O pin direction control
W0	B0 B1			
W1	B2			